



Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
Sheet	1	of	12	Application Number	10/541,957
				Filing Date	July 8, 2005
				First Named Inventor	Walter Fix
				Group Art Unit	2832
				Examiner	Not assigned
				Attorney Docket Number	411000-138

U.S. PATENT DOCUMENTS					
Examiner Initial*	Cite No. ¹	Document Number Number-Kid Code ² (if known)	Publication- Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
/SC/		US-2002/0022284	02-21-2002	Heeger	
		US-2002/0053320	05-09-2002	Duthaler	
		US-2002/0056839	05-16-2002	Joo et al.	
		US-2002/0068392	06-06-2002	Lee et al.	
		US-2002/0170897	11-21-2002	Hall	
		US-2002/0018911	02-00-2002	Bernius et al.	
		US-2002/0195644	12-26-2002	Dodabalapur et al.	
		US-2002/025391	02-28-2002	Angelopoulos	
		US-2002/130042	09-19-2002	Moerman et al.	
		US-2003/0112576	08-19-2003	Brewer et al.	
		US-2003/059987	03-27-2003	Sirringhaus et al.	
		US-2004/0002176	0101-2004	Xu	
		US-2004/0013982	01-00-2004	Jacobson et al.	
		US-2004/0026689	02-00-2004	Bemds et al.	
		US-2004/0084670	05-06-2004	Tripsas et al.	
		US-2004/0211329	10-00-2004	Funahata et al.	
		US-3,512,052	12-12-1970	Machver et al.	
		US-3,769,096	10-30-1973	Ashkin	
		US-3,955,098	05-04-1976	Kawamoto	
		US-4,302,648	11-24-1981	Sado et al.	
		US-4,442,019	04-19-1984	Marks	
		US-4,926,052	05-15-1990	Hatayama	
		US-4,865,187	09-12-1989	Craig	
		US-5,173,835	12-22-1992	Comett et al.	
/SC/		US-5,206,525	04-27-1993	Yamamoto et al.	

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number

Substitute for form 1449A/PTO <div style="text-align: center;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</div> <div style="text-align: center;">(Use as many sheets as necessary)</div>				<div style="text-align: center;">Complete if Known</div> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 60%;">Application Number</td> <td>10/541,957</td> </tr> <tr> <td>Filing Date</td> <td>July 8, 2005</td> </tr> <tr> <td>First Named Inventor</td> <td>Walter Fix</td> </tr> <tr> <td>Group Art Unit</td> <td>2832</td> </tr> <tr> <td>Examiner</td> <td>Not assigned</td> </tr> <tr> <td>Attorney Docket Number</td> <td>411000-138</td> </tr> </table>		Application Number	10/541,957	Filing Date	July 8, 2005	First Named Inventor	Walter Fix	Group Art Unit	2832	Examiner	Not assigned	Attorney Docket Number	411000-138
Application Number	10/541,957																
Filing Date	July 8, 2005																
First Named Inventor	Walter Fix																
Group Art Unit	2832																
Examiner	Not assigned																
Attorney Docket Number	411000-138																
Sheet	2	of	12														

/SC/		US-5,259,926	11-09-1993	Kuwabara et al.	
		US-5,321,240	06-14-1994	Takihira	
		US-5,347,144	09-13-1994	Garnier et al.	
		US-5,395,504	03-07-1995	Saurer et al.	
		US-5,480,839	01-02-1996	Ezawa et al.	
		US-5,486,851	01-23-1996	Gehner et al.	
		US-5,502,396	03-26-1996	Desarzens	
		US-5,546,889	08-20-1999	Wakita et al.	
		US-5,569,879	10-29-1996	Gloton et al.	
		US-5,574,291	11-12-1996	Dodabalapur et al.	
		US-5,578,513	11-00-1996	Maegawa	
		US-5,580,794	12-03-1996	Allen	
		US-5,629,530	05-13-1997	Brown et al.	
		US-5,630,986	05-20-1997	Charlton et al.	
		US-5,652,645	07-29-1997	Jain	
		US-5,691,089	11-25-1997	Smayling	
		US-5,729,428	03-17-1998	Sakata et al.	
		US-5,854,139	12-29-1998	Kondo et al.	
		US-5,869,972	02-09-1999	Birch et al.	
		US-5,892,244	04-06-1999	Tanaka et al.	
		US-5,946,551	08-31-1999	Dimitrakopoulos	
		US-5,987,048	10-19-1999	Fromson et al.	
		US-5,970,318	10-19-1999	Choi et al.	
		US-5,973,598	10-26-1999	Beigel	
		US-5,997,817	12-07-1999	Crismore et al.	
		US-6,036,919	03-14-2000	Thym et al.	
		US-6,045,977	04-04-2000	Chandross et al.	
↓		US-6,060,338	05-09-2000	Tanaka et al.	
/SC/		US-6,083,104	07-04-2000	Choi Kei Fung	

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number

Substitute for form 1449A/PTO <h2 style="text-align: center;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h2> <p style="text-align: center;">(Use as many sheets as necessary)</p>				<h3 style="text-align: center;">Complete if Known</h3> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 50%;">Application Number</td> <td>10/541,957</td> </tr> <tr> <td>Filing Date</td> <td>July 8, 2005</td> </tr> <tr> <td>First Named Inventor</td> <td>Walter Fix</td> </tr> <tr> <td>Group Art Unit</td> <td>2832</td> </tr> <tr> <td>Examiner</td> <td>Not assigned</td> </tr> <tr> <td>Attorney Docket Number</td> <td>411000-138</td> </tr> </table>		Application Number	10/541,957	Filing Date	July 8, 2005	First Named Inventor	Walter Fix	Group Art Unit	2832	Examiner	Not assigned	Attorney Docket Number	411000-138
Application Number	10/541,957																
Filing Date	July 8, 2005																
First Named Inventor	Walter Fix																
Group Art Unit	2832																
Examiner	Not assigned																
Attorney Docket Number	411000-138																
Sheet	3	of	12														

/SC/		US-6,133,835	10-17-2000	DeLeeuw et al.	
↓		US-6,207,472	03-27-2001	Calligari et al.	
↓		US-6,215,130	04-00-2001	Dodabalapur	
↓		US-6,251,513	06-26-2001	Rector et al.	
↓		US-6,284,562	09-00-2001	Batlogg et al.	
↓		US-6,300,141	10-09-2001	Segal et al.	
↓		US-6,322,736	11-00-2001	Bao	
↓		US-6,335,539	10-19-1999	Dimitrakopoulos et al.	
↓		US-6,340,822	01-22-2002	Brown et al.	
↓		US-6,344,662	02-05-2002	Dimitrakopoulos et al.	
↓		US-6,403,396	06-11-2002	Gudesen et al.	
↓		US-6,429,450	08-06-2002	Mutsaers et al. et al.	
↓		US-6,517,955	02-00-2005	Jacobsen et al.	
↓		US-6,852,583	02-08-2005	Bemds et al.	
↓		US-6,903,958	06-07-2005	Bemds et al.	
/SC/		US-6,960,489	11-01-05	Bemds et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initial*	Cite No.†	Foreign Patent Document Country Code ² Number ⁴ Kind Code ⁵ (if known)	Publication- Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		DE 33 38 597	05-02-1985	GAO Gesellschaft	See the attached IDS letter for all of the foreign language documents	
/SC/		DE 100 06 257 (title page only)	09-14-2000	IBM		
↓		DE 100 12 204 (title page only)	09-20-2001	Siemens		
/SC/		DE 100 33 112 (title page only)	01-24-2002	Siemens		

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number

Substitute for form 1449A/PTO <h2 style="text-align: center;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h2> <p style="text-align: center;">(Use as many sheets as necessary)</p>				<h3 style="text-align: center;">Complete if Known</h3> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 50%;">Application Number</td> <td>10/541,957</td> </tr> <tr> <td>Filing Date</td> <td>July 8, 2005</td> </tr> <tr> <td>First Named Inventor</td> <td>Walter Fix</td> </tr> <tr> <td>Group Art Unit</td> <td>2832</td> </tr> <tr> <td>Examiner</td> <td>Not assigned</td> </tr> <tr> <td>Attorney Docket Number</td> <td>411000-138</td> </tr> </table>		Application Number	10/541,957	Filing Date	July 8, 2005	First Named Inventor	Walter Fix	Group Art Unit	2832	Examiner	Not assigned	Attorney Docket Number	411000-138
Application Number	10/541,957																
Filing Date	July 8, 2005																
First Named Inventor	Walter Fix																
Group Art Unit	2832																
Examiner	Not assigned																
Attorney Docket Number	411000-138																
Sheet	4	of	12														

/SC/		DE 100 43 204 (title page only)	04-04-2002	Siemens		
		DE 100 45 192	04-04-2002	Siemens AG		
		DE 100 47 171	04-18-2002	Siemens AG		
		DE 100 58 559	05-29-2002	Interactive Biotech.		
		DE 100 61 297 (title page only)	06-27-2002	Siemens		
		DE 101 17 663	10-17-2002	Samsung SDI Co.		
		DE 101 20 687	10-31-2002	Siemens AG		
		DE 102 19 905	12-04-2003	Osram Opto Semicond.		
		DE 198 16 860	11-18-1999	Deutsche Telekom		
		DE 198 52 312 (title page only)	05-20-1999	Nintendo Co.		
		DE 199 18 193 (title page only)	11-25-1999	Cambridge Display		
		DE 199 21 024 (title page only)	11-16-2000	Eichelmann		
		DE 199 33 757	01-25-2001	Giesecke & Devrient		
		DE 199 35 527	02-08-2001	Giesecke & Devrient		
		DE 199 37 282	03-01-2001	Siemens		
		DE 424 38 32	06-30-1994	Daimler-Benz		
		DE 695 19 782 (title page only)	01-03-2001	News Datacom Ltd.		
		EP 0 128 529	12-19-1984	BASF		
		EP 0 268 370 A2	05-25-1988	Canon Kabushiki Kaisha		X
		EP 0 268 370 A3	05-25-1988	Canon Kabushiki Kaisha		X
		EP 0 350 179	01-10-1990	W & T Avery Ltd.		X
		EP 0 442 123	08-21-1991	Neste OY		X
		EP 0 460 242	12-11-1991	Nippon Petrochemicals		X
		EP 0 501 456 A2	09-02-1992	Sony		X
		EP 0 501 456 A3	09-02-1992	Sony		X
		EP 0 511 807	11-04-1992	GEC Avery Ltd.		X
		EP 0 528 662	02-24-1993	Kabushiki Kaisha Toshiba		X
		EP 0 615 256	09-23-1998	Koninklijke Philips		
		EP 0 685 885	12-06-1995	Hitachi Metals		X
		EP 0 716 458	06-12-1996	AT&T Corp.		X
		EP 0 785 578 A2	07-23-1997	AT & T Corp.		X
V /SC/		EP 0 785 578 A3	07-23-1997	AT & T Corp.		X

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
				Application Number	10/541,957
				Filing Date	July 8, 2005
				First Named Inventor	Walter Fix
				Group Art Unit	2832
				Examiner	Not assigned
Sheet	5	of	12	Attorney Docket Number	411000-138

/SC/		EP 0 962 984	12-08-1999	Lucent Technologies		X
		EP 0 966 182	12-22-1999	LG Electronics		X
		EP 0 979 715	02-18-2000	Adolf Illig Maschinenbau		
		EP 0 981 165	02-23-2000	Lucent Technologies		X
		EP 0 989 614 A2	03-29-2000	Sel Semiconductor		X
		EP 1 048 912	11-02-2000	Miele & Cie		
		EP 1 052 594	11-15-2000	Sokymat S.A.		
		EP 1 065 725 A2	01-03-2001	Sel Semiconductor		X
		EP 1 065 725 A3	01-03-2001	Sel Semiconductor		X
		EP 1 083 775	03-14-2001	Seiko Epson		
		EP 1 102 335 A2	05-23-2001	Lucent Technologies		X
		EP 1 103 916 (title page only)	05-30-2001	Infinson Technologies		
		EP 1 104 035 A2	05-30-2001	Lucent Technologies		X
		EP 1 134 694	09-19-2001	Infineon Technologies		
		EP 1 224 999 (title page only)	07-24-2002	Sumitomo Heavy Ind.		X
		EP 1 237 207	09-04-2002	Fuji Photo Film Co.		X
		EP 1 318 084	06-11-2003	Nippon Sanso Corp.		
		FR2793089	11-03-2000	Liger Rene		
		GB 2 058 482	04-08-1981	Shin-Etsu Polymer Co.		X
		GB 723,598	02-09-1955	N V Phillips Gloeilampenfabrieken		X
		GR2001P03239 (not available)				
		GR2001P20024 (not available)				
		JP 01169942 (abstract)	07-05-1989	Hitachi Ltd.		X
		JP 05152560 (abstract)	06-18-1993	Sumitomo Chem Co.		
		JP 05259434	10-05-1993	Nisha Printing		X
		JP 05347422 (abstract)	12-27-1993	Fujitsu Ltd.		X
		JP 08197788 (abstract)	08-08-1995	Hitachi Koki		X
		JP 09083040 (abstract)	03-28-1997	Sharp Corp.		X
		JP 09320760 (abstract)	12-12-1997	Matsushita Electric Ind.		X
		JP 09320760	12-12-1997	Matsushita Electric Ind.		
V /SC/		JP 10026934 (abstract)	01-27-1998	Toshiba Chem. Corp.		X

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number

Substitute for form 1449A/PTO <h2 style="text-align: center;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h2> <p style="text-align: center;">(Use as many sheets as necessary)</p>				Complete if Known	
				Application Number	10/541,957
				Filing Date	July 8, 2005
				First Named Inventor	Walter Fix
				Group Art Unit	2832
				Examiner	Not assigned
Sheet	6	of	12	Attorney Docket Number	411000-138

ISC/		JP 2001085272 (abstract)	03-30-2001	Matsushita Electric Ind.		X
		JP 2969184	12-20-1991	_____		
		JP 362065477A	03-24-1987	Toshiba		X
		JP 54069392 (abstract)	06-04-1979	Sakamoto Mitsuru		X
		JP 54069392 (abstract)	06-04-1979	NEC Corp.		X
		JP 60117769 (abstract)	06-25-1985	Fujitsu Ltd.		
		JP 61001060 (abstract)	01-07-1986	Hitachi Koki		X
		JP 61167854	07-29-1986	Murata Mfg. Co. Ltd.		X
		WO 00/33063	08-08-2000	Moorfodge Biotech		X
		WO 00/36686	08-22-2000	E Ink Corp.		X
		WO 00/79617	12-28-2000	Cambridge University		X
		WO 01/03126	01-11-2001	Regents of U. of CA		X
		WO 01/06442	01-25-2001	Ylp		X
		WO 01/08241	02-01-2001	E Ink Corporation		X
		WO 01/15233	03-01-2001	Koninklijke Philips		X
		WO 01/17029	03-08-2001	E Ink Corp.		X
		WO 01/17041	03-08-2001	E Ink Corp.		X
		WO 01/27998	04-19-2001	Koninklijke Philips		X
		WO 01/46987	06-26-2001	Plastic Logic Ltd.		
		WO 01/47044 A2	06-26-2001	Plastic Logic Limited		X
		WO 01/47044 A3	06-26-2001	Plastic Logic Limited		X
		WO 01/47045	06-26-2001	Plastic Logic		X
		WO 01/73109 A2	10-24-2001	Iverness Medical		X
		WO 01/73109 A3	10-24-2001	Iverness Medical		X
		WO 02/05360	01-17-2002	Siemens AK		X
		WO 02/05361	01-17-2002	3M Innovative Prop.		X
		WO 02/065557 A1	08-22-2002	Siemens		
		WO 02/065557 A1 abstract	08-22-2002	Siemens		X
		WO 02/071139	09-12-2002	Acreo AB		X
		WO 02/071505	09-12-2002	Acreo AB		x
		WO 02/076924	10-03-2002	Nisshinbo Industries		
ISC/		WO 02/091495	11-14-2002	Coatue Corp.		X

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
				Application Number	10/541,957
				Filing Date	July 8, 2005
				First Named Inventor	Walter Fix
				Group Art Unit	2832
				Examiner	Not assigned
Sheet	7	of	12	Attorney Docket Number	411000-138

/SC/		WO 02/095805 A2	11-28-2002	Plastic Logic Limited		X
		WO 02/095805 A3	11-28-2002	Plastic Logic Limited		X
		WO 02/099907	12-12-2002	Siemens		X
		WO 02/099908	12-12-2002	Siemens		
		WO 02/15284	02-21-2002	Siemens AK		
		WO 02/19443	03-07-2002	Siemens		
		WO 02/19443 (abstract)	03-07-2002	Siemens		X
		WO 02/29912	04-11-2002	Cambridge University		X
		WO 02/43071	05-30-2002	Thin Film Electronics		X
		WO 02/47183	06-13-2002	Siemens		
		WO 02/47183 (abstract)	06-13-2002	Siemens		X
		WO 03/046922	06-05-2003	Infineon Technologies		
		WO 03/067680	08-14-2003	Canon Kabushiki Kaisha		X
		WO 03/069552	08-21-2003	Rafsec Oy		X
		WO 03/081671	10-02-2003	Siemens AK		
		WO 03/095175	11-20-2003	ZBD Displays Ltd.		
		WO 04/042837 A2 abstract	05-21-2004	Siemens		X
		WO 04/042837 A3	05-21-2004	Siemens		X
		WO 04/047144 A2	06-03-2004	Siemens		X
		WO 04/047144 A2 (abstract)	06-03-2004	Siemens		X
		WO 04/047144 A3	06-03-2004	Siemens		X
		WO 04/047144 A3 (abstract)	06-03-2004	Siemens		X
		WO 04/7194 A2	06-03-2004	Siemens		X
		WO 04/7194 A2 (abstract)	06-03-2004	Siemens		X
		WO 04/7194 A3	06-03-2004	Siemens		X
		WO 2004/032257	04-15-2004	Leonhard Kurz GmbH		
		WO 2004/083859	09-30-2004	Platform Diagnostics		
		WO 93/16491	08-19-1993	Kopin Corp.		X
		WO 94/17556	08-04-1994	FCI-Fiberchem		X
		WO 95/06240	03-02-1995	Metrika Laboratories		X
		WO 95/31831 (title page only)	11-23-1995	Philips Electronics		X
/SC/		WO 96/02924	02-01-1996	Oryx Techn Corp.		X

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number

Substitute for form 1449A/PTO <h2 style="text-align: center; margin: 10px 0;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h2> <p style="text-align: center; margin-top: 10px;">(Use as many sheets as necessary)</p>				<h3 style="text-align: center; margin: 0;">Complete if Known</h3> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 60%;">Application Number</td> <td>10/541,957</td> </tr> <tr> <td>Filing Date</td> <td>July 8, 2005</td> </tr> <tr> <td>First Named Inventor</td> <td>Walter Fix</td> </tr> <tr> <td>Group Art Unit</td> <td>2832</td> </tr> <tr> <td>Examiner</td> <td>Not assigned</td> </tr> <tr> <td>Attorney Docket Number</td> <td>411000-138</td> </tr> </table>		Application Number	10/541,957	Filing Date	July 8, 2005	First Named Inventor	Walter Fix	Group Art Unit	2832	Examiner	Not assigned	Attorney Docket Number	411000-138
Application Number	10/541,957																
Filing Date	July 8, 2005																
First Named Inventor	Walter Fix																
Group Art Unit	2832																
Examiner	Not assigned																
Attorney Docket Number	411000-138																
Sheet	8	of	12														

ISC/		WO 96/19792	06-27-1998	Trustees of Princeton	X
ISC/		WO 97/12349	04-03-1997	DeRivaz	X
		WO 97/18944	05-29-1997	Gov't of USA	X
		WO 98/18156	04-30-1998	Steag Microtech	
		WO 98/18156 (abstract)	04-30-1998	Steag Microtech	X
		WO 98/18186 (title page only)	04-30-1998	Erico Lightning	X
		WO 98/40930	09-17-1998	Precision Dynamics	X
		WO 99/07189	02-11-1999	Cambridge	X
		WO 99/10929 (title page only)	03-04-1999	Koninklijke Philips	X
		WO 99/10939	03-04-1999	Koninklijke Philips	X
		WO 99/21233	04-29-1999	Regents of U California	X
		WO 99/40631	08-12-1999	Opticom USA	X
		WO 99/53371	10-21-1999	E Ink Corp.	X
		WO 99/54936	10-28-1999	Cambridge Display	X
		WO 99/54936 Corrected Version	10-28-1999	Cambridge Display	
ISC/		WO 99/66540	12-23-1999	Opticom ASA	X

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number

Substitute for form 1449A/PTO <h2 style="text-align: center;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h2> <p style="text-align: center;">(Use as many sheets as necessary)</p>			Complete if Known		
			Application Number	10/541,957	
			Filing Date	July 8, 2005	
			First Named Inventor	Walter Fix	
			Group Art Unit	2832	
			Examiner	Not assigned	
Sheet	9	of	12	Attorney Docket Number	411000-138

NON-PATENT LITERATURE DOCUMENTS			
Examiner Initial	Cite No.	Document Title	
/SC/		ASSADI A, et al., "Field-Effect Mobility of Poly (3-Hexylthiophene) Dept. of Physics and Measurement Technology, Received 3 March 1988; accepted for Publication 17 May 1988	X
		BAO, Z. et al., "High-Performance Plastic Transistors Fabricated by Printing Techniques", Chem. Mater Vol. 9, No. 6, 1997, pp 1299-1301.	X
		BRABEC, C.J. et al, "Photoinduced FT-IR spectroscopy and CW-photocurrent measurements of conjugated polymers and fullerenes blended into a conventional polymer matrix", Solar Energy Materials and Solar Cells, 2000 Elsevier Science B.V., pages 19-33.	X
		BRABEC, C.J. et al., "Photovoltaic properties of a conjugated polymer/methanofullerene composites embedded in a polystyrene matrix", Journal of Applied Physics, Vol 85, No. 9, 1999, pages 6866 - 6872.	X
		BRAUN D., et al, "Visible light emission from semiconducting polymer diodes", American Institute of Physics, Applied Physics Letters 58, May 6, 1991, pages 1982 - 1984.	X
		BROWN, A.R. et al., "Field-effect transistors made from solution-processed organic semiconductors", Elsevier Science, S.A., Synthetic Metals 88 (1997) pp. 37-55	X
		BROWN, A.R., "Logic Gates Made from Polymer Transistors and Their Use in Ring Oscillators", Science, Vol. 270, November 10, 1995, pp 972 - 974	X
		CHEN, Shiao-Shien et al., "Deep Submicrometer Double-Gate Fully-Depleted SOI PMOS Devices: A Concise Short-Channel Effect Threshold Voltage Model Using a Quasi-2D Approach", IEEE Transaction on Electron Devices, Vol. 43, No. 9, September 1996	X
		CHEN, X.L. et al., "Morphological and Transistor Studies of Organic Molecular Semiconductors with Anisotropic Electrical Characteristics", American Chemical Society, 2001, Chem. Mater. 2001, 13, 1341-1348.	X
		COLLET J. et al., "LOW VOLTAGE, 30 NM CHANNEL LENGTH, ORGANIC TRANSISTORS WITH A SELF-ASSEMBLED MONOLAYER AS GATE INSULATING FILMS", APPLIED PHYSICS LETTERS, AMERICAN INSTITUTE OF PHYSICS, NEW YORK, US, Bd 76, Nr. 14, 3. april 2000 (2000-04-03), Seiten 1941-1943, XP000950589, ISSN:0003-6951, das ganze Dokument	X
		CRONE, B. ET AL, "Large-scale complementary integrated circuits based on Organic transistors", Nature, Vol. 403, Feb. 3, 2000, PP. 521 -	X
		DAI, L. et al, "Photochemical Generation of Conducting Patterns in Polybutadiene Films", Macromolecules, Vol. 29, No. 1, 1996, pages 282-287, XP 001042019, the whole document	X
		DAI, L. et al., "I ₂ -Doping" of 1,4-Polydienes", Elsevier Science S.A., Synthetic Metals 69 (1995), pp 563-566.	X
/SC/		DAI, L. et al., "Conjugation of Polydienes by Oxidants Other Than Iodine", Elsevier Science S.A., Synthetic Metals 88 (1997) 1893-1894.	X

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Complete if Known	
				Application Number	MASTER LIST III – includes all to 8/4/05 for file 124
				Filing Date	
				First Named Inventor	
				Group Art Unit	
				Examiner Name	
Sheet	10	Of	12	Attorney Docket Number	

/SC/		DE LEEUW D.M. et al., "Polymeric integrated circuits and light-emitting diodes", Electron Devices Meeting, 1997. Technical Digest, International, Washington, DC, USA 7-10 Dec. 1997, New York, NY, USA, IEEE, US 7 December 1997.	X
		DODABALAPUR, A. et al., Organic smart pixels", American Institute of Physics, Applied Physics Letters, Vol. 73, No. 2, July 13, 1998, pp. 142 – 144.	X
		FIX, W. et al., "Fast Polymer Integrated Circuits Based on a Polyfluorene Derivative", ESSDERC 2002, 2002, pp. 527-529.	X
		Fraunhofer Magazin- Polytronic Chips Von der Rolle, 4.2001, Pages 8-13	
		GARNIER F et al., "Vertical Devices Architecture By Molding Of Organic-Based Thin Film Transistor", Applied Physics Letters, American Institute Of Physics. XP000784120, issn: 0003-6951 abbildung 2	X
		GARNIER et al., "Conjugated Polymers and Oligomers as Active Material For Electronic Devices", Synthetic Metals, Vol. 28, 1989	X
		GELINCK, G.H. et al., "High-Performance All-Polymer Integrated Circuits", Applied Physics Letters, v. 77, 2000, pp. 1487-1489.	X
		GOSAIN, D.P., "Excimer laser crystallized poly-Si TFT's on plastic substrates", Second International Symposium on Laser Precision Microfabrication, May 16-18, 2001, Singapore, Vol. 4426, pages 394 – 400.	X
		HALLS, J.J. M., et al., "Efficient photodiodes from interpenetrating polymer networks", Nature, Vol. 376, August 10, 1995, pp. 498 – 500.	X
		HARSANYI G. ET AL, "Polytronics for biotronics:unique possibilities of polymers in biosensors and BioMEMS", IEEE Polytronic 2002 Conference, June 23, 2002, pages 211-215	
		HEBNER, T.R. et al., "Ink-jet printing of doped polymers for organic light emitting devices", American Institute of Physics, Applied Physics Letters, Vol. 72, no. 5, February 2, 1998, pages 519-521.	X
		HWANG J D et al., "A Vertical Submicron Sic thin film transistor", Solid State Electronics, Elsevier Science Publishers, Barking, GB, Bd. 38, NR. 2.1. February 1995 (1995-02-01), Seiten 275-278, XP004014040, ISSN:0038-1101, Abbildung 2	X
		IBM Technical Disclosure Bulletin, "Short-Channel Field-Effect Transistor", IBM Corp., New York, US, Bd. 32, Nr. 3A, 1.August 1989 (1989-08-01), Seiten 77-78, XP000049357, ISSN:0018-8689, das ganze Dokument	X
		KAWASE, T. et al., "Inkjet Printed Via-Hole Interconnections and Resistors for All-Polymer Transistor Circuits", Advanced Materials 2001, 13, No. 21, November 2, 2001, pp 1601 – 1605.	X
		KLAUK, H. et al., "Fast Organic Thin Film Transistor Circuits", IEEE Electron Device Letters, Vol. 20, no. 6, pages 289-291	X
		KLAUK, H. et al., "Pentacene Thin Film Transistors and Inverter Circuits", 1997 International Electron Devices Meeting Technical Digest, pages 539-542, December 1997	X
		KOEZUKA, H. et al., "Macromolecular Electronic Device", Mol. Cryst. Liq. Cryst. 1994, Vol. 2555, pp. 221-230.	X
		KUMAR, Anish et al., "Kink-Free Polycrystalline Silicon Double-Gate Elevated-Channel Thin-Film Transistors", IEEE Transactions on Electron Devices, Vol. 45, No. 12, December 1998	X
↓ /SC/		LIDZEY, D. G. et al., "Photoprocessed and Micropatterned Conjugated Polymer LEDs", Synthetic Metals, V. 82, 1996, pp. 141-148	X

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
				Application Number	MASTER LIST III – includes all to 8/4/05 for file 124
				Filing Date	
				First Named Inventor	
				Group Art Unit	
				Examiner Name	
Sheet	11	Of	12	Attorney Docket Number	

/SC/		LOWE, J. et al., "Poly (3—(2—Acetoxyethyl)Thiophene): A Model Polymer for Acid-Catalyzed Lithography", Synthetic Metals, Elsevier Sequoia, Lausanne, CH, Bd. 85, 1997, Seiten 1427-1430.	X
		LU, Wen et al., "Use of Ionic Liquids for π -Conjugated Polymer Electrochemical Devices", Science, Vol 297, 2002, pages 983 – 987/	X
		LUCENT TECHNOLOGIES, "Innovation marks significant milestone in the development of electronic paper", Cambridge, MA and Murray Hill, NJ, November 20, 2000. XP-002209726.	X
		MIYAMOTO, Shoichi et al.: "Effect of LDD Structure and Channel Poly-Si Thinning on a Gate-All-Around TFT (GAT) for SRAM's, IEEE Transactions on Electron Devices. Vol. 46, No. 8, August 1999	X
		OELKRUG, D. et al., "Electronic spectra of self-organized oligothiophene films with 'standing' and 'lying' molecular units", Elsevier Science S.A., 1996, Thin Solid Films 284-270	X
		QIAO, X. et al., "The FeCl ₃ -doped poly(3-alkylthiophenes) in solid state", Elsevier Science, Synthetic Metals 122 (2001) pp 449—454.	
		REDECKER, M. et al., "Mobility enhancement through homogeneous nematic alignment of a liquid-crystalline polyfluorene", 1999 American Institute of Physics, Applied Physics Letters, Vol. 74, number 10, pp. 1400-1402.	X
		ROGERS J A et al.: "Low-Voltage 0.1 μ m Organic Transistors and Complementary Inverter Circuits Fabricated with a Low-Cost Form of Near-Field Photolithography", Applied Physics Letters, American Institute of Physics. New York, US, Bd. 75, Nr. 7, 16. August 1999 (1999-08-16), Seiten 1010-1012, XP000934355, ISSN: 003-6951, das ganze Dokument	X
		ROGERS, J. A. et al.: "Printing Process Suitable for Reel-to-Reel Production of High-Performance Organic Transistors and Circuits", Advanced Materials, VCH, Verlagsgesellschaft, Weinheim, DE, Bd. 11, Nr. 9, 5. Juli 1999 (1999-07-05), Seiten 741-745, P000851834, ISSN: 0935-9648, das ganze Dokument	X
		ROMAN et al., "POLYMER DIODES WITH HIGH RECTIFICATION:", Applied Physics Letters, Vol. 75, No. 21, November 22, 1999	X
		SANDBERG, H. et al, "Ultra-thin Organic Films for Field Effect Transistors", SPIE Vol. 4466, 2001, pp. 35 – 43.	X
		SCHOEBEL, "Frequency Conversion with Organic-On-Inorganic Heterostructured Diodes", Extended Abstracts of the International Conference on Solid State Devices and Materials, September 1, 1997	X
		SCHRODNER M. ET AL., "Plastic electronics based on Semiconducting Polymers", First International IEEE Conference on Polymers and Adhesives in Microelectronics and Photonics. Incorporating Poly, Pep & Adhesives in Electronics. Proceedings (Cat. No. 01TH8592), First International IEEE Conference on Polymers and Adhesives in Micr, Seiten 91 – 94.	X
		SHAHEEN, S.E., et al., "Low band-gap polymeric photovoltaic devices", Synthetic Metals, Vol 121, 2001, pages 1583-1584.	X
		TAKASHIMA, W. et al., Electroplasticity Memory Devices Using Conducting Polymers and Solid Polymer Electrolytes", Polymer International, Melbourne, 1992, pages 249 – 253.	XX
↓ /SC/		VELU, G. et al. "Low Driving Voltages and Memory Effect in Organic Thin-Film Transistors With A Ferroelectric Gate Insulator", Applied Physics Letters, American Institute of Physics, New York, Vol 79, No. 5, 2001, pages 659 – 661.	X

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
				Application Number	MASTER LIST III – includes all to 8/4/05 for file 124
				Filing Date	
				First Named Inventor	
				Group Art Unit	
				Examiner Name	
Sheet	12	Of	12	Attorney Docket Number	

/SC/		WANG, Hsing Lin et al., "Conducting Polymer Blends: Polythiophene and Polypyrrole Blends with Polystyrene and Poly (bisphenol A carbonate), American Chemical Society, 1990 pp. 1053 – 1059.	X	
↓		WANG, Yading et al., "Electrically Conductive Semilinterpenetrating Polymer Networks of Poly(3-octylthiophene)", Macromolecules 1992, Vol 25, pages 3284 – 3290.	X	
		YU, G. et al., "Dual-function semiconducting polymer devices: Light-emitting and photodetecting diodes", American Institute of Physics, Applied Physics Letter 64, March 21, 1994, pages 1540 – 1542.	X	
	/SC/	ZHENG, Xiang-Yang et al., "Electrochemical Patterning of the Surface of Insulators with Electrically Conductive Polymers", J. Electrochem. Soc., v. 142, 1995, pp L226-L227.	X	
Examiner Signature		/Swapneel Chhaya/	Date Considered	05/11/2007

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. † Applicant's unique citation designation number (optional). ‡ See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. § Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ¶ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ** Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. *** Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Walter Fix et al.
SERIAL NO: 10/541,957
FILED: July 8, 2005
EXAMINER Not assigned ART UNIT 2832
FOR: ORGANIC FIELD EFFECT TRANSISTOR AND INTEGRATED
CIRCUIT
ATTY DKT NO.: 411000-138 CUSTOMER NO.: 27162

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

DISCLOSURE STATEMENT UNDER 37 CFR 1.56

SIR:

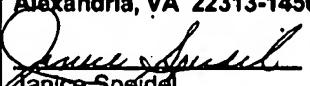
This paper is to bring to the attention of the PTO the following commonly owned copending U.S. applications, all of which are related in different respects to organic electronic devices and/or method of making such devices such as transistors, diodes, integrated circuits and the like. Many of these applications also have one or more common inventors. The enclosed PTO 1449 lists these applications. It is respectfully requested that the Examiner consider and make of record all of the cited applications listed on the attached PTO 1449.

<u>Application No.</u>	<u>Title</u>	<u>Inventors</u>	<u>Atty. Dkt. No.</u>
10/344,951	Organic Field-Effect Transistor (OFET), A Production Method Therefor, An Integrated Circuit Constructed From the Same and Their Uses	Adolf Bernds et al.	411000-99
10/362,932	Organic Field Effect Transistor, Method for Structuring an OFET and Integrated Circuit	Adolf Bernds et al	411000-110

10/380,113	Organic Rectifier, Circuit, RFID Tag and Use of an Organic Rectifier	Adolf Bernds et al.	411000-106
10/381,032	Electrode and/or Conductor Track for Organic Components and Production Method Thereof	Adolf Bernds et al.	411000-105
10/433,959	Organic Field Effect Transistor, Method For Structuring an OFET and Integrated Circuit	Adolf Bernds	411000-108
10/433,961	Device For Detecting and/or Transmitting at Least One Environmental Influence, Method for Producing Said Device and Use Thereof	Wolfgang Clemens et al.	411000-111
10/467,636	Organic Field Effect Transistor With a Photostructured Gate Dielectric, Method for the Production and Use Thereof in Organic Electronics	Adolf Bernds et al.	411000-104
10/473,050	Device With At Least Two Organic Electronic Components and Method for Producing the Same	Adolf Bernds et al.	411000-113
10/479,234	Organic Field Effect Transistor, Method for Production and Use Thereof in the Assembly of Integrated Circuits	Adolf Bernds et al.	411000-101
10/479,238	Method For Producing Conductive Structures by Means of Printing Technique, and Active Components Produced Therefrom For Integrated Circuits	Adolf Bernds et al.	411000-100
10/492,922	Insulator for An Organic Electronic Component	Erwann Guillet et al.	411000-115
10/492,923	Electronic Unit, Circuit Design for the Same and Production Method	Wolfgang Clemens et al.	411000-114
10/498,610	Organic Field Effect Transistor with Offset Threshold Voltage and the Use Thereof	Walter Fix et al.	411000-119
10/508,640	Logic Component Comprising Organic Field Effect Transistors	Walter Fix et al.	411000-120
10/508,737	Device and Method for Laser Structuring Functional Polymers and	Adolf Bernds et al.	411000-121
10/517,750	Substrate for an Organic Field Effect Transistor, Use of the Substrate, Method of Increasing the Charge Carrier Mobility and Organic Field Effect Transistor (OFET)	Wolfgang Clemens et al.	411000-122
10/523,216	Electronic Component Comprising Predominantly Organic Functional Materials And A Process For The Production Thereof	Adolf Bernds et al.	411000-123
10/523,487	Electronic Device	Wolfgang Clemens et al.	411000-124
10/524,646	Organic Component for Overvoltage Protection and Associated Circuit	Walter Fix et al.	411000-127

10/533,756	Organic Electronic Component with High-Resolution Structuring and Process for the Production Thereof	Wolfgang Clemens et al.	411000-128
10/534,678	Measuring Apparatus for Determining an Analyte in a Liquid Sample	Wolfgang Clemens et al.	411000-129
10/535,448	Organic Electronic Component Comprising Semi-Conductive Functional Layer and Method for Producing Said Component	Wolfgang Clemens et al.	411000-131
10/535,449	Organic Electronic Component Comprising the Same Organic Material for at Least Two Functional Layers	Adolf Bernds et al.	411000-132
10/344,926	An Electronic Circuit Having an Encapsulated Organic-Electronic Component, and a Method for Making an Encapsulated Organic-Electronic Component	Wolfgang Clemens et al.	411000-133
10/541,815	Organo-Resistive Memory Unit	Axel Gerlt et al.	411000-136
10/541,956	Board or Substrate for an Organic Electronic Device and Use Thereof	Wolfgang Clemens et al.	411000-137
10/541,957	Organic Field Effect Transistor And Integrated Circuit	Walter Fix et al.	411000-138
10/543,561	Organic Storage Component and Corresponding Triggering Circuit	Wolfgang Clemens et al.	411000-139
10/542,678	Organic Electronic Component and Method For Producing Organic Electronic Devices	Adolf Bernds et al.	411000-140
10/542,679	Use of Conductive Carbon Black/Graphite Mixtures for the Production of Low-Cost Electronics	Adolf Bernds et al.	411000-141

The Commissioner is authorized to charge payment of any fees associated with this communication or credit any overpayment to Deposit Account No. 03-0678.

FIRST CLASS CERTIFICATE	
I hereby certify that this correspondence is being deposited on the date set forth below with the U.S. Postal Service as First Class Mail, postage prepaid, in an envelope addressed to:	
Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450	
 Janice Speidel	Nov. 2, 2005 Date

272930v1

Respectfully submitted,
Walter Fix et al.

by William Squire, Reg. No. 25,378
 Attorney for applicants
 CARELLA, BYRNE, BAIN, GILFILLAN,
 CECCHI, STEWART & OLSTEIN
 5 Becker Farm Road
 Roseland, NJ 07068
 Tel: (973)994-1700
 Fax: (973)994-1744

Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Application Number	Not assigned
				Filing Date	Herewith 10/541957
				First Named Inventor	Walter Fix
				Group Art Unit	Not assigned
				Examiner Name	Not assigned
Sheet	1	Of	2	Attorney Docket Number	411000-138

U.S. PATENT DOCUMENTS					
Examiner Initial*	Cite No. ¹	Document Number Number-Kid Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
/SC/		US-6,197,663	03-06-2001	Chandross	See search report
/SC/		US-6,326,288	12-04-2001	Bomefeld	
/SC/		US-6,362,509	12-26-2002	Hart	
		US-			
		US-			
		US-			
		US-			
		US-			
		US-			
		US-			
		US-			
		US-			
		US-			
		US-			

FOREIGN PATENT DOCUMENTS						
Examiner Initial*	Cite No. ¹	Foreign Patent Document Country Code ³ Number ⁴ Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Y ⁶
/SC/		EP 1 102 335 A2	05-23-2001	Lucent Technologies	See search report	X
/SC/		EP 1 104 035A2	05-30-2001	Lucent Technologies		X
/SC/		WO 01/17029	03-08-2001	E Ink Corporation		X
/SC/		WO 01/27988	04-19-2001	Koninklijke Philips		X
/SC/		WO 99/66540	12-23-1999	Opticom ASA		X

Examiner Signature	/Swapneel Chhaya/	Date Considered	05/11/2007
--------------------	-------------------	-----------------	------------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
				Application Number	10/541,957
				Filing Date	July 8, 2005
				First Named Inventor	Walter Fix
				Group Art Unit	2832
				Examiner	Not assigned
Sheet	1	of	2	Attorney Docket Number	411000-138

U.S. PATENT DOCUMENTS					
Examiner Initial*	Cite No. ¹	Document Number Number-Kid Code ² (if known)	Publication- Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
					See accompanying Disclosure Statement filed herewith
ISC/	133	US-10/344,926	02/12/2004	Adolf Bernds et al.	
	99	US-10/344,951	02/12/2004	Adolf Bernds et al.	
	110	US-10/362,932	10/02/2003	Adolf Bernds et al.	
	106	US-10/380,113	09/25/2003	Adolf Bernds et al.	
	105	US-10/381,032	02/12/2004	Adolf Bernds et al.	
	108	US-10/433,959	04/01/2004	Adolf Bernds et al.	
	111	US-10/433,961	04/01/2004	Wolfgang Clemens et al.	
	109	US-10/451,108	05/13/2004	Mark Giles et al.	
	104	US-10/467,638	11/04/2004	Adolf Bernds et al.	
	113	US-10/473,050	05/20/2004	Adolf Bernds et al.	
	101	US-10/479,234	12/30/2004	Adolf Bernds et al.	
	100	US-10/479,238	10/20/2004	Adolf Bernds et al.	
	115	US-10/492,922	03/03/2005	Erwann Bullet et al.	
	114	US-10/492,923	12/23/2004	Wolfgang Clemens et al.	
	119	US-10/498,610	9/29/2005	Walter Fix et al.	
	120	US-10/508,640	N/A	Walter Fix et al.	
	121	US-10/508,737	5/19/2005	Adolf Bernds et al.	
	122	US-10/517,750	10/13/2005	Wolfgang Clemens et al.	
	123	US-10/523,216	N/A	Adolf Bernds et al.	
	124	US-10/523,487	N/A	Wolfgang Clemens et al.	
	127	US-10/524,646	N/A	Walter Fix et al.	
ISC/	128	US-10/533,756	N/A	Wolfgang Clemens et al.	

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
				Application Number	10/541,957
				Filing Date	July 8, 2005
				First Named Inventor	Walter Fix
				Group Art Unit	2832
				Examiner	Not assigned
Sheet	2	of	2	Attorney Docket Number	411000-138

/SC/	129	US-10/534,678	N/A	Wolfgang Clemens et al.	
↓	131	10/535,448	N/A	Wolfgang Clemens et al.	
	132	10/535,449	N/A	Walter Fix et al.	
	136	US-10/541,815	N/A	Axel Gerlt et al.	
	137	US-10/541,956	N/A	Wolfgang Clemens et al.	
	138	US10/541,957	N/A	Walter Fix et al.	
	139	US-10/543,561	N/A	Wolfgang Clemens et al.	
	140	US-10/542,678	N/A	Adolf Bernds et al.	
	141	US-10/542,679	N/A	Adolf Bernds et al.	
	/SC/				
	Examiner Signature		/Swapneel Chhaya/		Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional). 2 See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2

272942v1